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Sheet 1 of 2

Application Number	New National Stage
Filing Date	May 10, 2005
First Named Inventor	Anthony SPENCER
Art Unit	Unassigned
Examiner Name	Unassigned
Attorney Docket Number	0120-034

[illegible][illegible]

Examiner Signature	/Yong Choe/	Date Considered	06/03/2007
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Complete if Known	
		Application Number	New National Stage 10/534430
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		First Named Inventor	Anthony SPENCER
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Sheet 2	of 2	Attorney Docket Number	0120-034

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/Y.C./	1	International Search Report of PCT/GB2003/04867 dated February 8, 2005	
/Y.C./	2	Ingersoll, S., et al., "Dataflow computation with intelligent memories emulated on field-programmable gate arrays (FPGAs)," Microprocessors and Microsystems, Vol. 26, No. 6,	
		August 2002, pp. 263-280, Great Britain	
/Y.C./	3	Elliott, D., et al., "Computational RAM: Implementing Processors in Memory," IEEE Design & Test of Computers, IEEE Computers Society,	
		Vol. 16, No. 1, January 1999, pp. 32-41, United States	

Examiner Signature	/Yong Choe/	Date Considered	06/03/2007
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